Question: When organizing the CellularRAM memory as described in the laboratory write-up, how many words are not displayed or “wasted”? – **6,144 words**

Question: Which bits of a frame buffer word store the “Green” pixel values for the “odd” pixel (i.e., the pixel that is in an odd column)? **Bits 12-10**

Question: Which memory address stores the pixel information for location (x, y) = (590, 157) in image #14 (i.e., the image using the 5-bit image value of 01110)? **01110010011101100100111**

**0x393B27**

Question: What image number, row, and column are associated with memory address 0x1C5D2C?

**111000101110100101100**

**Image number – 11100 – 28 base 10**

**Image row – 010111010 – 186 base 10**

**Image col – 100101100 – 300 base 10**

Question: Summarize any changes you had to make to your SRAM memory controller.

**So I had to modify my SRAM controller to only have 4 read states (I had 5 before), after rerunning it with the testbench from lab9, I felt confident that it was working properly with 4 states. Then I modified the next state logic to return directly state ‘r1’ rather than idle if mem was low and rw was high. Rerunning the testbench again from lab9 and it appears that at least nothing broke.**